

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Peter Warnes, et al.

Appl. No. : 09/523,877

Filed : March 13, 2000

For : **METHOD AND APPARATUS  
FOR MULTI-MODE JUMP  
DELAY SLOT CONTROL IN  
A PIPELINED PROCESSOR**

Examiner : Huisman, David J.

Group Art Unit: 2183

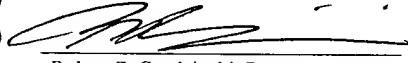


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PATENT TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

April 1, 2005  
(Date)

  
Robert F. Gazdzinski, Reg. No. 39,990

**TRANSMITTAL LETTER**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

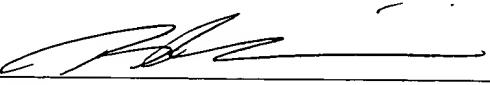
Dear Sir:

Enclosed for filing in the above-identified application are:

1. Information Disclosure Statement citing twenty-three (23) references (2 pages);
2. Form PTO-1449 enclosing twenty-three (23) references (1 page);
3. Check in the sum of \$180.00; and
4. Return prepaid postcard.

Respectfully submitted,  
GAZDZINSKI & ASSOCIATES

Dated: 4/15/05

By: 

Robert F. Gazdzinski, Esq.

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ARC.005R12



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Peter Warnes, et al. )  
Appl. No. : 09/523,877 )  
Filed : March 13, 2000 )  
For : **METHOD AND APPARATUS FOR** )  
      **MULTI-MODE JUMP DELAY** )  
      **SLOT CONTROL IN A PIPELINED** )  
      **PROCESSOR** )  
Examiner : Huisman, David J. )  
Group Art Unit: 2183 )

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27299

PATENT TRADEMARK OFFICE

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing twenty-three (23) references that are also enclosed.

This Information Disclosure Statement is being filed before the mailing date of either a final action under 37 C.F.R. § 1.113, or a notice of allowance under 37 C.F.R. § 1.311, whichever occurs first, and is accompanied by the fee set forth in 37 C.F.R. § 1.17(p).

Pursuant to 37 C.F.R. § 1.97(g) and (h), Applicant makes no representation that a search has been made or that the information cited is considered to be material to patentability. Additionally, inclusion on this list is not an admission that any of the cited documents are prior

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01 FC:1806

**App. No. :** 09/523,877  
**Filed :** March 13, 2000

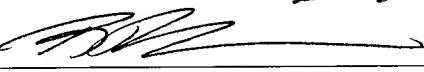
art in this application. Further, Applicant makes no representation regarding the completeness of this list or that better art does not exist.

The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 and § 1.17 which may be required to Deposit Account No. 501423. This sheet is attached in duplicate.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated: 4/1/05

By: 

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## INFORMATION DISCLOSURE STATEMENT

BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT  
Peter Warnes, et al.FILING DATE  
March 13, 2000GROUP  
2183

## U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE     | NAME               | CLASS | SUBCLASS | FILING DATE (IF APPROPRIATE) |
|------------------|-----------------|----------|--------------------|-------|----------|------------------------------|
| 1                | 5,493,508       | 2/20/96  | Dangelo, et al.    |       |          |                              |
| 2                | 5,535,331       | 7/9/96   | Swoboda et al.     |       |          |                              |
| 3                | 5,544,067       | 8/6/96   | Rostoker, et al.   |       |          |                              |
| 4                | 5,555,201       | 9/10/96  | Dangelo, et al.    |       |          |                              |
| 5                | 5,801,958       | 9/1/98   | Dangelo et al.     |       |          |                              |
| 6                | 5,812,416       | 9/22/98  | Gupte, et al.      |       |          |                              |
| 7                | 5,867,399       | 02/1999  | Rostoker, et al.   |       |          |                              |
| 8                | 6,173,434       | 01/2001  | Wirthlin, et al.   |       |          |                              |
| 9                | 6,324,678       | 11/2001  | Dangelo, et al.    |       |          |                              |
| 10               | 6,378,123       | 04/2002  | Dupenloup          |       |          |                              |
| 11               | 6,408,428       | 06/2002  | Schlansker, et al. |       |          |                              |
| 12               | 6,421,818       | 07/2002  | Dupenloup, et al.  |       |          |                              |
| 14               | 6,477,683 B1    | 11/5/02  | Killian, et al.    |       |          |                              |
| 14               | 6,477,697 B1    | 11/5/02  | Killian, et al.    |       |          |                              |
| 15               | 6,760,888       | 7/6/04   | Killian, et al.    |       |          |                              |
| 16               | 6,862,563       | 03/01/05 | Hakewill, et al.   |       |          |                              |

## FOREIGN PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE    | COUNTRY | CLASS | SUBCLASS | TRANSLATION |    |
|------------------|-----------------|---------|---------|-------|----------|-------------|----|
|                  |                 |         |         |       |          | YES         | NO |
| 17               | 2 308 470       | 6/97    | GB      |       |          |             |    |
| 18               | WO 97 13209 A   | 04/1997 | WIPO    |       |          |             |    |

| EXAMINER INITIAL | OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)  |  |
|------------------|---|--|
| 19               | Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039  |  |
| 20               | Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)   |  |
| 21               | Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.   |  |
| 22               | Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.  |  |
| 23               | Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268 |  |

| EXAMINER   | DATE CONSIDERED |
|--|-----------------|
| *EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT. |                 |